

IN THE CLAIMS:

Claims 6, 7, 13, 14, 20, 21 and 27 were previously cancelled. Claims 1, 16 and 18 have been amended herein. All of the pending claims are presented below. This listing of claims will replace all prior versions and listings of claims in the application. Please enter these claims as amended.

1. (Currently amended) An emitter array for a display device having a periphery, a middle, and an interior, each having a plurality of pixels, at least one emitter in each pixel having substantially a same height as a corresponding pixel in another emitter of the emitter array, a material for each emitter comprising amorphous silicon, the at least one emitter and the another emitter having a size within about 0.15 microns of each other in a range from about 3.25 % to within 9.5 % of each other, the periphery having a distribution of emitter heights of not less than 1.4 microns, 1.5 microns, and not ~~reater~~ greater than 1.6 microns, the middle having a distribution of emitter heights of not less than 1.5 microns, 1.6 micron, and not greater than 1.7 microns, and the interior having a distribution of emitter heights of not less than 1.5 micron, 1.6 microns, and not greater than 1.7 microns.

2. (Previously presented) The array of claim 1, wherein the display device is a field emission display device.

3. (Previously presented) The array of claim 2, wherein the at least one emitter has a size ranging from about 1.5 microns to about 1.7 microns.

4. (Previously presented) The array of claim 3, wherein the at least one emitter has a size of about 1.6 microns.

5. (Previously presented) The array of claim 4, wherein a size of all emitters in the display device ranges from about 1.5 microns to about 1.7 microns.

6. (Cancelled)

7. (Cancelled)

8. (Previously presented) An emitter array for a display device having a plurality of pixels, at least one emitter in each pixel having substantially a same height as a corresponding pixel in another emitter of the emitter array, a material for each emitter comprising amorphous silicon, the at least one emitter and the another emitter having a size within about 0.15 microns of each other, the at least one emitter having a height of no less than 1.4 microns.

9. (Previously presented) The array of claim 8, wherein the display device is a field emission display device.

10. (Previously presented) The array of claim 9, wherein the at least one emitter has a size ranging from about 1.5 microns to about 1.7 microns.

11. (Previously presented) The array of claim 10, wherein the at least one emitter has a size of about 1.6 microns.

12. (Previously presented) The array of claim 11, wherein a size of all emitters in the display device ranges from about 1.5 microns to about 1.7 microns.

13. (Cancelled)

14. (Cancelled)

15. (Previously presented) An emitter for a display device having a plurality of pixels, an emitter in each pixel having substantially a same height as another emitter in a corresponding pixel of the emitter display, a material for each emitter comprising amorphous silicon, the emitter and the another emitter having a size within about 0.15 microns of each other in a range from about 3.25 % to within 9.5 % of each other, the at least one emitter having a height of no less than 1.4 microns.

16. (Currently amended) The display device of ~~claim 1,~~ claim 15, wherein the display device comprises a field emission display device.

17. (Previously presented) The display device of claim 16, wherein the emitter has a size ranging from about 1.5 microns to about 1.7 microns.

18. (Currently amended) The display device of ~~claim 3,~~ claim 17, wherein the emitter has a size of about 1.6 microns.

19. (Previously presented) The display device of claim 18, wherein the size of substantially all emitters in the display device ranges from about 1.5 microns to about 1.7 microns.

20. (Cancelled)

21. (Cancelled)

22. (Previously presented) An emitter array having a plurality of pixels, an emitter in each pixel having substantially a same height as another emitter of a corresponding pixel of the emitter display, a material for each emitter comprising amorphous silicon, the emitter and the another emitter having a size within about 0.15 microns of each other in a range from about 3.25 % to within 9.5 % of each other, the at least one emitter having a height of no less than 1.4 microns.

23. (Previously presented) The array of claim 22, wherein the display device comprises a field emission display device.

24. (Previously presented) The array of claim 23, wherein the emitter has a size ranging from about 1.5 microns to about 1.7 microns.

25. (Previously presented) The array of claim 24, wherein the emitter has a size of about 1.6 microns.

26. (Previously presented) The array of claim 25, wherein the size of substantially all emitters in the display device ranges from about 1.5 microns to about 1.7 microns.

27. (Cancelled)